

Dr
a detecting circuit for detecting a change in voltage of said input signal; and
a correction circuit for changing an impedance of a parallel termination circuit
that is in parallel with said parasitic capacitance to reduce distortion of said input
signal.

14. (Twice Amended) Apparatus for reducing distortion of an input signal
applied to an input of a circuit operating at high frequency and having a parasitic
capacitance at said input, comprising:

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a first circuit element for selectively providing current to said parasitic
capacitance;

a second circuit element for selectively preventing discharge of said parasitic
capacitance; and

a control circuit monitoring said input signal for respectively turning on said
first circuit element and turning off said second circuit element when a positive going
edge of said input signal is detected and for turning off said first circuit element and
turning on said second circuit element when a negative going edge of said input signal
is detected.

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REMARKS

The present application contains claims 1-6, 8, 9, and 11-27.

It is noted that matters dealing with §112, second paragraph have been
clarified per applicant's amendment filed July 12, 2002.